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#### Jun 13, 2006

#### jtag "dummy bit"

\$\frac{\sqrt{c}}{c}\text{ http://www.freshpatents.com/Semiconductor-memory-device-dt200503 ... - 7:10pm

#### jtag dummy bit

- ☼ Details and datasheet on part: 5962-0053901QYA 7:09pm www.digchip.com/.../datasheet/477/5962-0053901QYA.php
- JRC (JTAG Route Controller ) Data Sheet 7:08pm www.icepp.s.u-tokyo.ac.jp/~takemoto/JRC/JRCDATA.pdf

#### jtag instructions

- ্ৰ'ং <u>Re: Pleeaase heeelp!!!</u> 7:06pm sat.connect.gi/myboard/messages/672.htm
- Structure and method for writing from a JTAG device with ... 7:05pm www.freepatentsonline.com/6925583.html
- A Brief Introduction to JTAG 7:05pm www.inaccessnetworks.com/.../jtag-intro/jtag-intro.html
- JTAG Guide 7:05pm www.dishnewbies.com/jtag.shtml

#### IEEE Standard Test Access Port and Boundary-Scan Architecture

- IEEE Standards Online Archives: Test Technology Standards 6:37pm standards.ieee.org/catalog/olis/arch\_testtech.html
- √ JTAG A Technical Overview TAP Signals and Instructions 6:36pm www.xjtag.com/jtag-technical-guide.php
- JTAG Boundary Scan Basics White paper 6:36pm www.engr.udayton.edu/.../ece446/notes/jtag/jtag1.html
- Citations: IEEE standard test access port and boundary-scan ... 6:35pm citeseer.ist.psu.edu/context/15176/0
- <u>"IEEE Standard Test Access Port and Boundary-Scan Architecture ...</u> 6:35pm webstore.ansi.org/.../product.asp?sku=IEEE+Std+1149.1-2001
- \*\* IEEE Standards Description: 1149.1-1990 6:35pm standards.ieee.org/.../testtech/1149.1-1990\_desc.html

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**IEEE 1149.1** 

SA: IEEE Standard Test Access Port and Boundary-Scan Architecture - 6:34pm www.techstreet.com/cgi-bin/detail?product\_id=923486

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Strand - Wikipedia, the free encyclopedia - 6:33pm en.wikipedia.org/wiki/JTAG